

# **Lynx QD Product Family**

### **Product Overview**



#### Mechanical Characteristics

- 4 row array in a compact 2 row footprint
  - Pin count: 12 to 80 Differential pairs
- Pitch: 0.9mm across all form factors
  - 48 diff. pairs//in<sup>2</sup> and length configurable
- Integrated Shielding Design





— Coplanar

### Electrical Characteristics

- Supports 56 Gb/s PAM4, PCIe Gen5
- Impedance: 92Ω
- Current rating: 1.5A per pin



— Right angle

### General Product Release: Q2'19

- 3 form factors
  - Coplanar, right angle, and vertical stacker receptacle
  - Vertical stack heights: 10mm, 40 position
  - Vertical Stacker Plugs: Based on customer demand



— Vertical Stacker

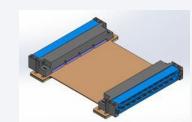
# **Lynx QD Product Family**

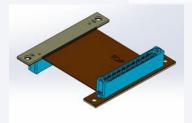
### **Product Release Status**



Currently Released	12 Position	20 Position	40 Position	60 Position	80 Position
Coplanar					
Right Angle				<b>⊘</b>	
Mezzanine/Stacker	Receptacle Only	Receptacle Only	Receptacle Only	Receptacle Only	Receptacle Only

Flex Options – Available Now!





# Lynx QD Product Family

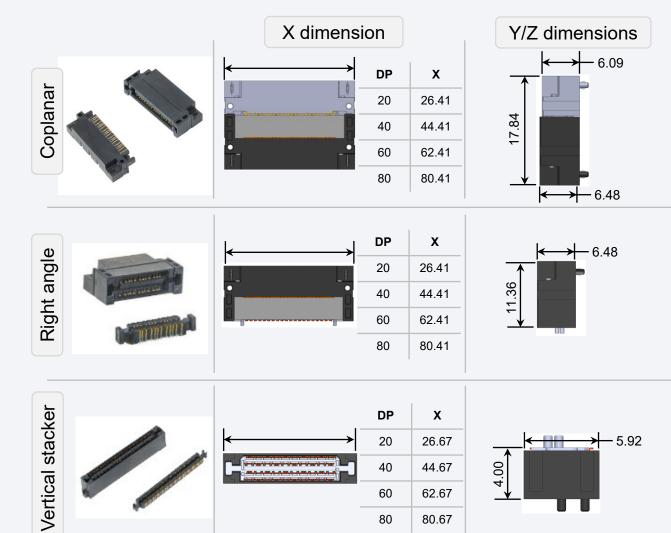
### Features and Benefits



Features	Benefits		
<ul> <li>Signal integrity</li> <li>56G PAM4 (28G NRZ) capable</li> <li>PCle Gen 5.0 compliant</li> </ul>	<ul> <li>Connector impedance tuned for 92 Ohms</li> <li>Low cross talk less than -40dB at 20 GHz</li> <li>Optimized for differential signaling</li> <li>Provides upgrade path for future system design</li> </ul>		
<ul> <li>Available in multiple form factors:</li> <li>Coplanar, Right Angle, Vertical stacker</li> <li>Highly dense connector – 4 rows</li> <li>Full product family offering</li> <li>Compact form factor</li> </ul>	<ul> <li>12 to 80 differential pairs, 36 to 240 conductors</li> <li>Stack heights from 4mm to 10mm (15mm in development)</li> <li>Configurable length</li> <li>Flexible pin assignment to support DP, SE and Power needs</li> <li>Low profile design allows for improved airflow in the system</li> </ul>		
<ul> <li>Mechanical robustness</li> <li>Large mechanical alignment features</li> <li>Integrated guide posts</li> <li>PCB alignment posts and clips (Right angle)</li> </ul>	<ul> <li>Guarantees proper alignment and mating</li> <li>Allows for multi-up connector configurations</li> <li>Allows for connector stability during reflow</li> </ul>		
<ul> <li>PCB</li> <li>Large routing channels</li> <li>Small footprint area</li> <li>Same footprint across entire product family</li> </ul>	<ul> <li>Allows for routing out of connector from one side</li> <li>2 signal layers</li> <li>Reduces PCB layers</li> <li>Smaller footprint increases allowable area for other components</li> </ul>		

### Mechanicals





20

40

60

80

- 4-Row 0.90mm pitch, 20 to 80 diff. pairs
- Integrated guidance features
- Normal force
  - 25g per pin EOL
- Mating force
  - 27g per pin typical
- Mechanical wipe
  - 0.50mm
- J-Lead SMT attachment
- Multi-up connector application capable

- 5.92

- Parallel or in-line
- PCB alignment and stabilizing feature

4.00

26.67

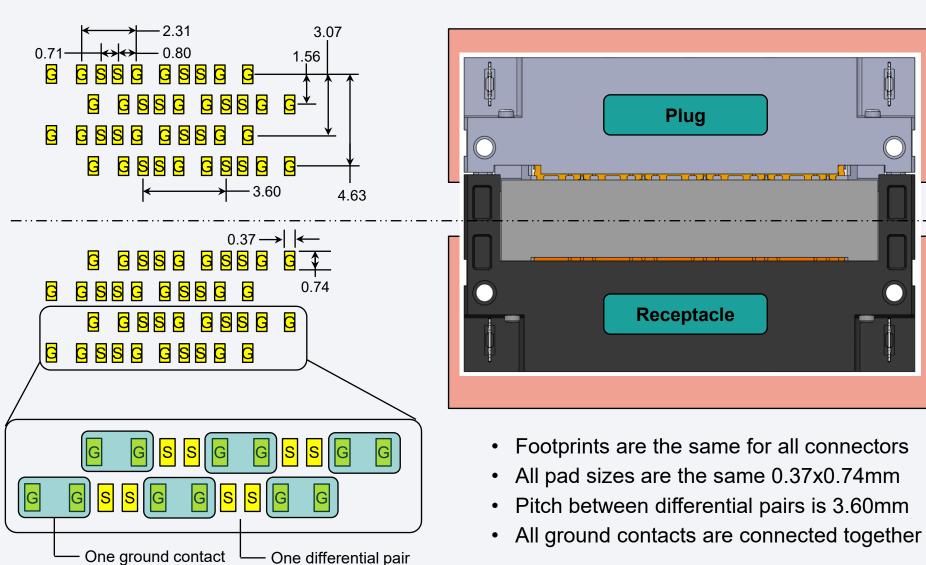
44.67

62.67

80.67

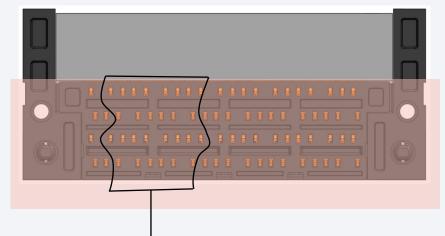
## **Footprint**

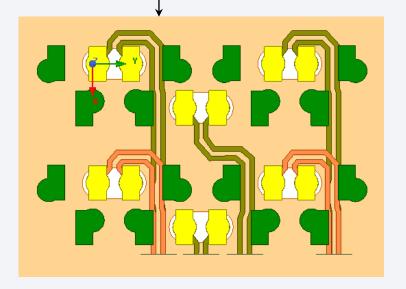




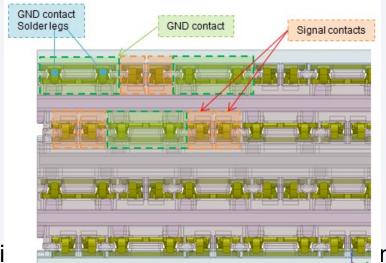
## Routing







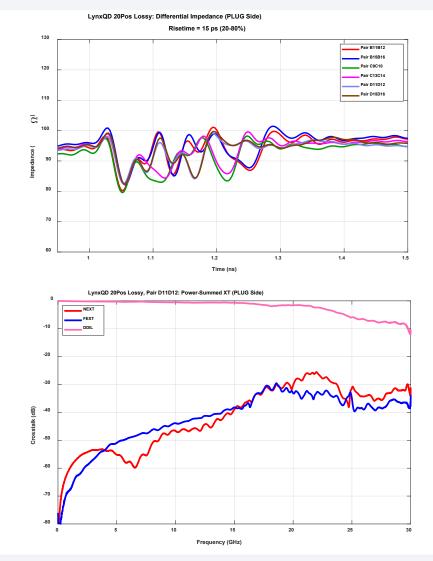
- Coplanar and right angle connectors can route out of connector on 2 layers
- Vertical stacker can route out of connector on 1 layer
  - Route out of connector from both sides



• Wi options

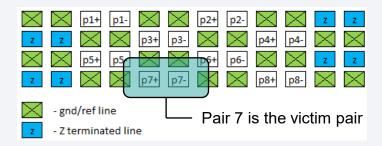
## Signal integrity - Coplanar





- 56G PAM 4 and PCIe Gen 5 performance
- Power sum cross talk below -40dB out to 15 GHz
- IL-PSNEXT margin 35 dB at 16 GHz
- IL-PSFEXT margin 35 dB at 16 GHz
- 92 Ohm impedance
- Current rating 1.5A per pin

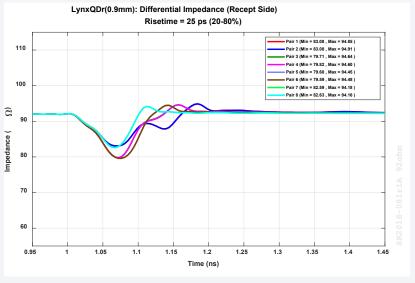
#### Signal integrity pin out

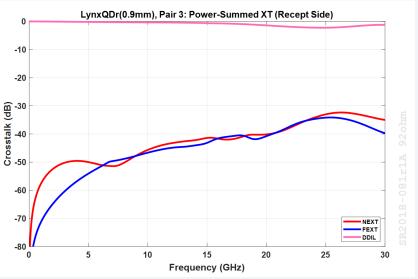


- Pair 7 is the victim
- 5 aggressors

## Signal integrity – Right angle

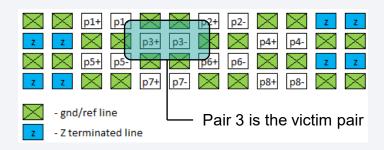






- 56G PAM 4 and PCIe Gen 5 performance
- Power sum cross talk below -40dB out to 20 GHz
- IL-PSNEXT margin 36 dB at 16 GHz
- IL-PSFEXT margin 35 dB at 16 GHz
- 92 Ohm impedance
- Current rating 1.5A per pin

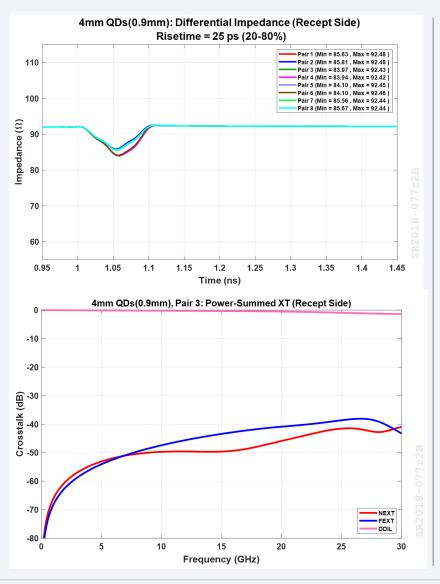
#### Signal integrity pin out



- Pair 3 is the victim
- 5 aggressors

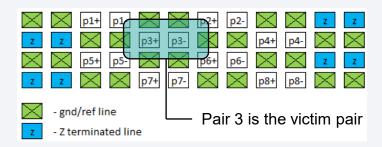
## Signal integrity – Vertical stacker





- 56G PAM 4 and PCIe Gen 5 performance
- Power sum cross talk below -40dB out to 20 GHz
- IL-PSNEXT margin 50 dB at 16 GHz
- IL-PSFEXT margin 41 dB at 16 GHz
- 92 Ohm impedance
- Current rating 1.5A per pin

#### Signal integrity pin out



- Pair 3 is the victim
- 5 aggressors

# **Value Proposition**

## Lynx QD – The Quad Differential Solution



### Key Customer Messages

- High speed, high density in multiple form factors
- Lynx QD is the "Problem Solver" ideal for many applications
- Paired with our Custom Assembly/Integration capability
- Built for quick-turn Design Wins

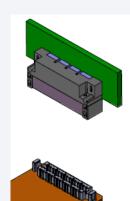


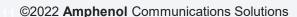
### Key Qualifying Questions

- What are your speed requirements for now and the future?
- Is your current solution flexible enough to meet future needs?
- Has available form factor been limiting your Design work?
- Does your current solution support all your signaling needs?
- Does your partner offer in house custom assembly integration?

### Lead with Mezzanine Flex Options

- Lynx QD interconnects with flexible circuit
- Connector/cable assemblies & Near Chip
- We have a dedicated Team to design custom solutions

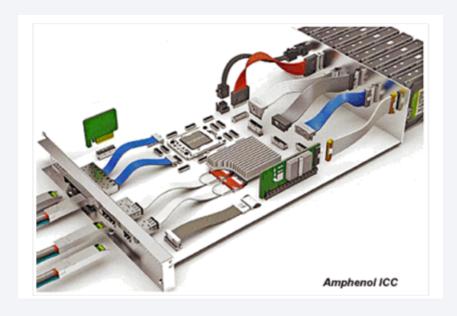




# **Target Markets**



- New Markets
  - Automotive
  - Medical and Diagnostics
  - Consumer Electronics/Cameras



## High Value Verticals

#### **TARGET MARKETS/APPLICATIONS**



Switches Routers Telecom Datacom



Rack Mount Server Blade Server Storage Server Datacenter



Car Multimedia Car Navigation Systems



Instrumentation and Medical



Medical Instrument Imaging Monitoring