	FC	APPLICATION SPECIFICATION	GS-20-128	
TITLE	HIGH POWER CARD		1 of 17	C DATE July, 16, 2012
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1. OBJECTIVE

This specification provides information and requirements for customer application of the HPCE Connector system. It is intended to provide general guidance for process development. It should be recognized that no single process will work under all customer applications and that customers should develop processes to meet individual needs. However, if the processes vary greatly from the recommended one, FCI cannot guarantee acceptable results.

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2. SCOPE

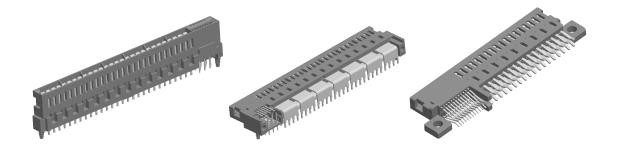
This specification provides information and requirements regarding application of the HPCE Connector System to printed circuit boards (PCB).

Table 1

	Vertical Receptacle, Solder & Press-fit Tail
High Power Card Edge	Right Angle Receptacle, Solder Tail
	Straddle Mount, Solder Tail

Figure 1

HPCE Connector System



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3. DRAWINGS AND APPLICABLE DOCUMENTS

- FCI Product Specification GS-12-604 (HPCE Connector system).
- UL/CSA File # E66906 -Volume 1 Section 124
- TUV certification # B 11 05 34414 012
- Applicable FCI Product Drawings

FCI product drawings and specifications are available by accessing the FCI website or contacting the FCI Technical Service. In the event of a conflict between this specification and the product drawing, the drawing takes precedence. Customers should refer to the latest revision level of FCI product drawings for appropriate product details.

4. GENERAL CUSTOMER INFORMATION

This document is a general application guide. If there is a conflict between the product drawings and this specification, the drawings take precedence.

4.1 PRODUCT APPLICATION

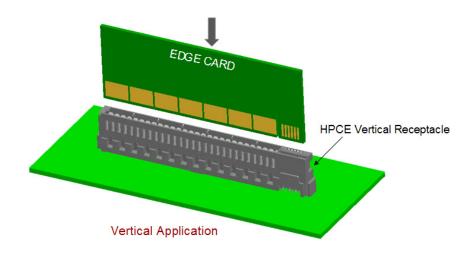


Figure 2

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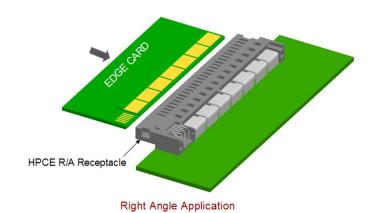
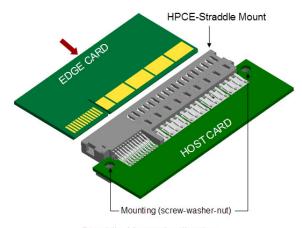


Figure 3



Straddle Mount Application

Figure 4

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HPCE has two options for connection to Printed Circuit Boards -- Press Fit and Solder to Board, available as follows:

Table 2

Product Configuration	Solder Tail	Press-Fit Tail
HPCE - Vertical Receptacle	х	Х
HPCE - R/A Receptacle	х	
HPCE - Straddle Mount	Х	

The HPCE Solder to Board Power and Signal contacts are compatible with several soldering processes, including wave soldering. They are versatile with many configurations to fit the individual needs of the client.

HPCE products are compatible with standard lead-free processing, including convection, infra-red, and vapor-phase reflow, and will withstand peak processing temperatures of 260°C for a period of 60 seconds without affecting form, fit, or function.

4.2 WIPE DISTANCE AND CONTACT SEQUENCING

The nominal wipe distance of the Signal contact is shorter than the Power contact by 1.27mm (0.050 inch).

Recommended minimum wipe is 1.5mm. I.0mm minimum wipe can be used in low shock/vibration situations where system boards and components are locked in place to eliminate relative motion.

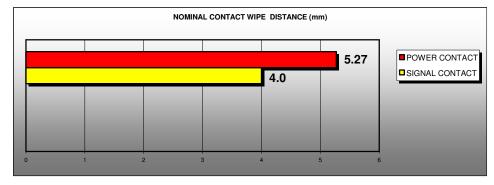


Table 3

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4.3. MATING ALIGNMENT

4.3.1 Under 100 mm connector length.

The HPCE connector design has not included a guide system therefore the design is not for a blind mate application. The mis-alignment allowance of the HPCE product is depended on the condition of the connector system and the chamfer of the edge card. Please see figure 5 and table 4 for explanation.

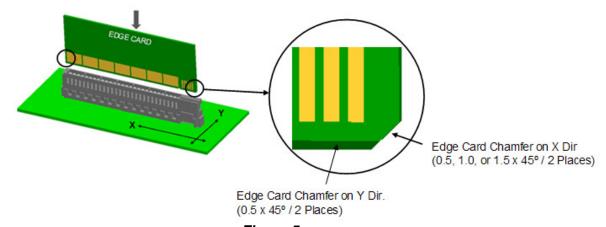
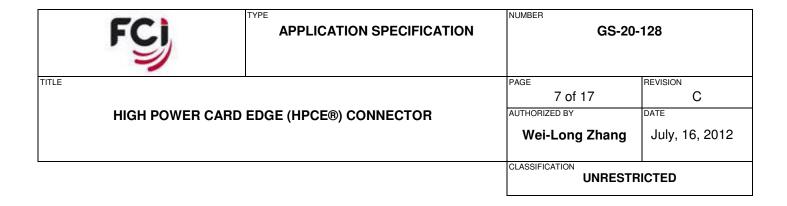


Figure 5

Connector Mating	Edge Card Mis-Alignment A Chamfer (mm) (mm)			
Condition	X	Υ	X	Y
One Side is in	0.50		1.85 ± 0.125	
Stationary condition, other side is in floating	1.00	0.50	2.35 ± 0.125	1.46 ± 0.13
condition.	1.50		2.85 ± 0.125	
Both Side are in Stationary Condition			0.15 ± 0.056	0.115 ± 0.07

Table 4

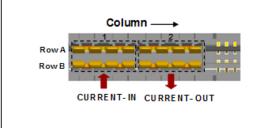


VOLTAGE RATING 4.4

The Maximum Working Voltage of the HPCE connector system is rated base on UL 60950-1 Second Edition Table 2N.

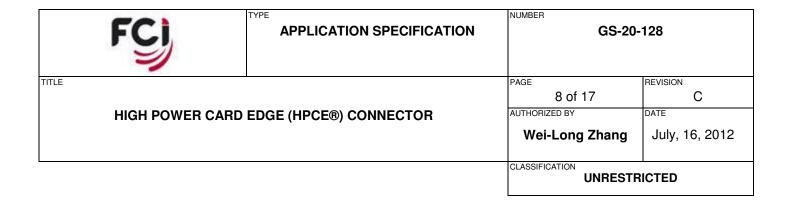
Pollution Degree : 2 (Office Environmental) Material Group: 1 (Based on UL rating)

HPCE - VERTICAL							
"DIFFEREN	"DIFFERENT" CURRENT PATH IN THE ROW OF POWER CONTACTS						
POSITION Tail Pitch (mm) between two contacts (Volt) AC RMS Working Voltage (Volt) (Volt)							
Power to Power	2.54	0.7	100	140			
	5.08	3.24	639	903			
Power to Signal	3.5	2.4	480	679			
Signal to Signal	1.27	0.41	12	17			
Gigital to Signal	2.54	1.68	338	475			



	HPC	E - VER	TICAL		
"SAME"	CURRENT PATH	INTHE R	OW OF POWER O	CONTACTS	
POSITION	Tail Pitch (mm) between two contacts	MCD (mm)	AC RMS Working Voltage (Volt)	DC / AC peak Working Voltage (Volt)	
Power to Power	2.7	2.1	420	594	
Power to Signal	3.5	2.4	480	679	
Signal to Signal	1.27	0.41	12	17	
Signal to Signal	2.54	1.68	336	475	
COlumn —— CURRENT - OUT Row B CURRENT - IN Row A					

Table 5



	HPCE	-RIGHT	ANGLE		
"DIFFEREN	T" CURRENT PA	ATH IN THE F	ROW OF POWER	CONTACTS	
POSITION	Tail Pitch (mm) between two contacts	MCD (mm)	AC RMS Working Voltage (Volt)	DC / A C peak Working Voltage (Volt)	
Power to Power	2.54	0.7	100	140	
rowel to rowel	5.08	3.24	639	903	
Power to Signal	3.5	2.4	480	679	
Circulta Circul	1.27	0.41	12	17	
Signal to Signal	2.54	1.68	336	475	
Column → Row A Row B					
	CURREN	T-IN CU	RRENT- OUT		

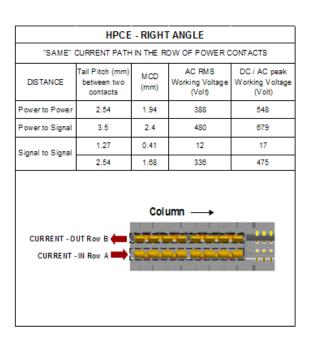


Table 6

FC	TYPE A	APPLICATION SPECIFICATION	NUMBER GS-20	GS-20-128		
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HPCE - STRADDLE MOUNT					
"DIFFERENT" CURRENT PATH IN THE ROW OF POWER CONTACTS					
POSITION	Tail Pitch (mm) between two contacts	MCD (mm)	AC RMS Working Voltage (Volt)	DC / AC peak Working Voltage (Volt)	
Power to Power	2.54	0.7	100	140	
rower to rower	5.08	3.24	639	903	
Power to Signal	3.5	2.4	480	679	
Signal to Signal	1.27	0.41	12	17	
Orginal to Orginal	2.54	1.68	336	475	
COlumn ————————————————————————————————————					
	CORRENT	-IN CUR	KENI-UUI		

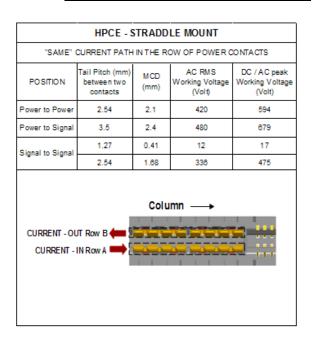


Table 7

4.5 CURRENT RATING

(Refer to FCI Product Specification GS-12-604 (for additional information)

Following are the current rating values of the HPCE connector system

Configuration	Number Power Pins (Fully Energized)	Edge Card	Air Temp. (Starting / C ²)	Still Air (No Forced Air)	T-Rise (max. / Cº)	Typical Current Rating (Single Power Pin/ Amps)	Signal Contact	Remark
24P	24					11.0 N/A 5.0z - 2 External Lave	5.0- 0.5-t	
40P	40					10.0	IN/A	5 Oz 2 External Layers test board
56P-12S	56	1.6 mm thick	6 mm thick Ambient Yes 30 9.0		9.0	2.3		
56P-12S	0					N/A		5 Oz, 2Layers. Only one Signal contact energized

Table 8

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4.6. MECHANICAL PROPERTIES

Mating/Un-mating forces

Configuration	Mating Force (N) (Max. Allowance)	Un-Mating Force (N) (Min. Allowance)
56P+12S	53.9	15.9
Single POWER Contact Beam	0.98	0.36
Single SIGNAL Contact Beam	0.22	0.06

Table 9

4.7. SAFETY

PREVENTION OF OPERATOR ACCESS TO ENERGIZED PARTS Reference UL60950 & IEC 60950-1 SECTION 2.1.1.1

UL and IEC specifications define three different probe designs to test for prevention of operator access to energized conductors (such as powered electrical contacts within an unmated connector). The two probes are referred to as follows:

- **Test Finger** (Figure 6)
- **Test Probe** (Figure 6)

The following sections show each of these test probes positioned as closely as possible to the mating side contacts of the Vertical HPCE, which will be located on the PCB and may be powered in an unmated state.

Although the Vertical HPCE connector system meets these probe requirements as noted, it is not recommended that the customer "hot plug" the edge card to the vertical receptacle.

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4.7.1 Test Finger

The **Test Finger** may not make contact with energized parts while the access doors and covers of the system enclosure are open. Separable connectors must be disconnected for this test. The figures show the tip of the **Test Finger** inserted into a Vertical HPCE capture window, showing that it is impossible for the probe (shown at the smallest size per specified tolerances) to touch the receptacle contacts.

4.7.2 Test Probe

The requirements for the **Test Probe** conditions are not as clearly specified by UL and IEC. However assuming the worst-case scenario where the HPCE connector is accessible, the following 3D model was created. This model shows that the Test Probe is very large compared to the Test Finger and will never come close to touching a powered contact within the representative receptacle.

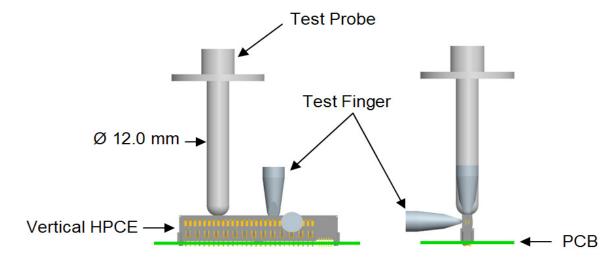


Figure 6

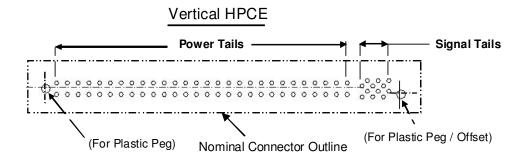
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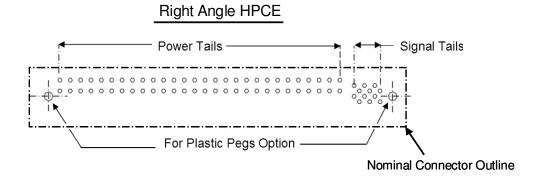
5. REQUIREMENT FOR CUSTOMERS PCB

Note: Generic figures are representative of all product configurations

For specifics of the PCB layout, refer to the customer drawing of the part number being applied.

5.1 PCB LAYOUT (See customer drawing for more details)





"Power and signal traces inside the connector zone need to be coated or under solder mask to protect against oxidation and minimize wear or damage during assembly and handling."

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RECOMMENDED HOST BOARD FOR STRADDLE MOUNT

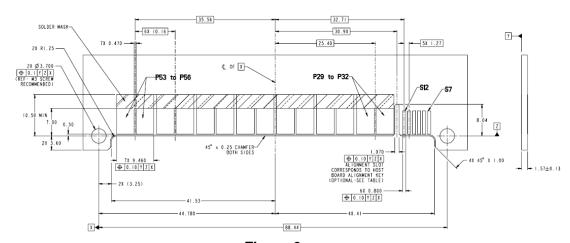


Figure 9

PLATED THROUGH HOLE REQUIREMENTS

CONTACT TYPE	TOP LAYER	TABLE (HPCE / SOLDER TAILS) PLATED THROUGH-HOLE REQUIREMENTS				
	DESCRIPTION	DRILLED HOLE DIAMETER	COPPER THICKNESS	TIN-LEAD THICKNESS	TIN THICKNESS	FINISHED HOLE DIAMETER
	TIN-LEAD	1.10-1.16 (1.15 DRILL)	0.025 - 0.050	0.005 - 0.015		0.94 - 1.10
POWER & SIGNAL	IMMERSION TIN	1.10-1.16 (1.15 DRILL)	0.025 - 0.050		0.9 - 1.5um	0.94 - 1.10
	COPPER (SEE NOTE 8)	1.10-1.16 (1.15 DRILL)	0.025 - 0.050			0.94 - 1.10

CONTACT TYPE	TOP LAYER	TABLE 2 (HPCE / PRESS-FIT TAILS) PLATED THROUGH-HOLE REQUIREMENTS				
CONTINCT	DESCRIPTION	DRILLED HOLE DIAMETER	COPPER THICKNESS	TIN-LEAD THICKNESS	TIN	FINISHED HOLE DIAMETER
	TIN-LEAD	0.81-0.86 (0.85 DRILL)	0.025 - 0.050	0.005 - 0.015	150	0.65 - 0.80
POWER & SIGNAL	IMMERSION TIN	0.81-0.86 (0.85 DRILL)	0.025 - 0.050	111	0.9 - 1.5um	0.70 - 0.80
	COPPER (SEE NOTE 8)	0.81-0.86 (0.85 DRILL)	0.025 - 0.050	**	100	0.70 - 0.80

Table 10

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For R/A solder tail termination, The values for the PCB are as follows

Tail lengths(mm)	Recommended PCB Thickness(mm)
2.60+/-0.35	1.57+/-0.25
3.25+/-0.40	1.57+/-0.25~2.25+/-0.25
4.05+/-0.40	3.05+/-0.25

Table 11

5.2 EDGE CARD LAYOUT(Ref. 56P-12S)

RECOMMENDED EDGE CARD FOOT PRINT

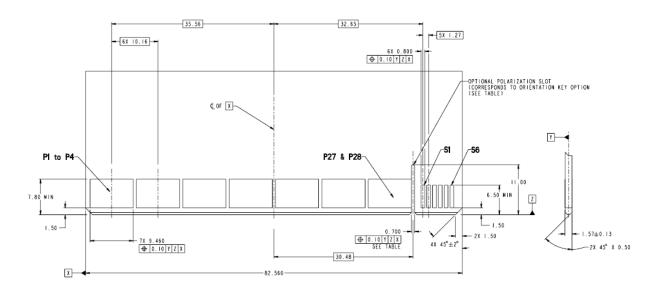


Figure 10

6 APPLICATION TOOLING

No application tooling is required for the Solder Tail Application tooling is required for the vertical Press fit. Tooling drawing number is 10119453.

For Solder Tail configuration, the total insertion force of two plastic pegs should not exceed 340 N (76 lbs)

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6.1. VERTICAL RECEPTACLE (SOLDER)

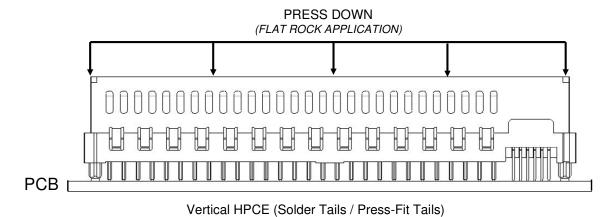
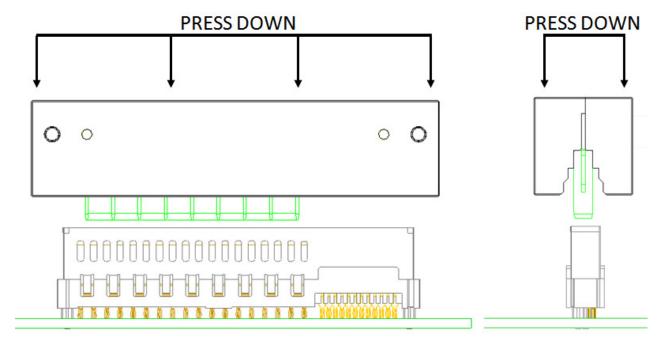


Figure 11

6.2. VERTICAL RECEPTACLE (PRESS-FIT) APPLICATION TOOLING



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Form E-3334 Rev A 02/12/01 V20603

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6.3 RIGHT ANGLE RECEPTACLE (STB)

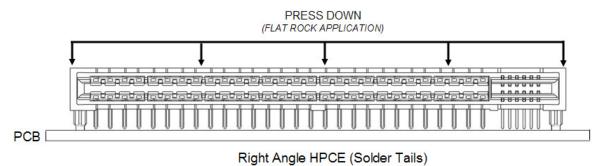


Figure 12

6.4 STRADDLE MOUNT (STB)

For Straddle Mount 56P-12S configuration, the insertion force on the host board side should not exceed $150\ N$.

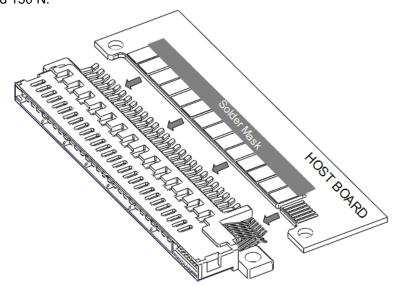


Figure 13

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7. REVISION RECORD

REV	PAGE	DESCRIPTION	EC#	DATE
Α	ALL	RELEASE APPLICATION SPECIFICATION	N/A	02/17/2011
В	3,6,12	ADDED UL/CSA/TUV CERTIFICATION (Page 3) AND MORE DETAILS TO MIS-ALIGNMENT ALLOWANCE (Table 4 / Page 6)- Added Note to PCB layout (page 12).		08/22/2011
С	9,14,15	ADDED R/A SOLDER TAIL TAIL LENGTH OPTION, ADD APPLICATION TOOLING FOR VERTICAL PF.	ELX-DG-012170	07/16/2012